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Application No.	Applicant(s)	
10/629,365	BAJWA ET AL.	
Examiner	Art Unit	
Tuan T. Nguyen	2824	
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6. ☐ Interview Summar Paper No./Mail D 7. ☒ Examiner's Amend 8. ☒ Examiner's Staten 9. ☒ Other <u>search histo</u>	y (PTO-413), ate dment/Comment nent of Reasons for Allowance ory printout.	
	Tuan T. Nguyen ars on the cover sheet with the OR REMAINS) CLOSED in this a per other appropriate communication is subject and MPEP 1308. atter filed on 10/28/05. der 35 U.S.C. § 119(a)-(d) or (f). been received. been received in Application No. uments have been received in this enterprise of this communication to file a replent of this application. atted. Note the attached EXAMINE is reason(s) why the oath or declar is be submitted. be submitted. and have been received (PTC) Amendment / Comment or in the draw is header according to 37 CFR 1.12-13. attention of BIOLOGICAL MATERIAL FOR THE DEPOSIT OF BIOLOGICAL FOR THE DEPOSIT OF BIOLOGICAL MATERIAL FOR THE DEPOSIT OF BIOLOGICAL FOR THE DEPOSIT OF BIOL	Examiner Tuan T. Nguyen 2824 ars on the cover sheet with the correspondence address OR REMAINS) CLOSED in this application. If not included or other appropriate communication will be mailed in due course. THIS GHTS. This application is subject to withdrawal from issue at the initiation and MPEP 1308. after filed on 10/28/05. der 35 U.S.C. § 119(a)-(d) or (f). been received. been received in Application No uments have been received in this national stage application from the of this application. after filed on this application. after filed on this application from the of this application to file a reply complying with the requirements entry of this application. after filed on this application is deficient. after filed on the attached EXAMINER'S AMENDMENT or NOTICE OF is reason(s) why the oath or declaration is deficient. after filed on the attached EXAMINER'S AMENDMENT or NOTICE OF is reason(s) why the oath or declaration is deficient. after filed on 10/28/05. after filed on 10/28/05.

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DETAILED ACTION

Election/Restrictions

1. Applicant's election without traverse of claims 1-13 and 20-27 in the reply filed on 10/28/05 is acknowledged.

2. Claims 14-19 have been withdrawn from further consideration pursuant to 37 CFR
1.142(b) as being drawn to a nonelected invention, there being no allowable generic or linking claim. Election was made without traverse in the reply filed on 10/28/05

Information Disclosure Statement

3. The information disclosure statement (IDS) submitted on 5/24/04 and 7/29/03 were filed after the mailing date of the present application on 7/29/03. The submission is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

EXAMINER'S AMENDMENT

The application has been amended as follows:

Page 6, line 2, change "220" to - 221 -

Page 11, line 27, change "Fig. 3" to - Fig. 3A -

Allowable Subject Matter

- 4. Claims 1-13 and 20-27 are allowed.
- 5. The following is an examiner's statement of reasons for allowance:

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The prior art of record (Proebsting US 6,462,998; Duh et al. US 6,795,360 and Wright et al. US 6,665,222) fail to disclose a serially-programmable integrated circuit (IC) comprising, in combination with other cited limitations, a memory array having a first width, the memory array comprising a plurality of primary columns and a plurality of redundant columns, the plurality of primary columns having a second width and the plurality of redundant columns having a third width; a data register coupled to receive the serial programming data from the first input connector, the data register comprising a shift register having a fourth width, the fourth width being a divisor of the second width and the third width; a bitline latch for loading a data word into the memory array, the bitline latch having the first width; and a column multiplexer for performing a loading operation to load data in parallel from the data register into the bitline latch as recited in claim 1.

Claims 2-13 are therefore allowed because of their dependency on claim 1.

The prior art of record further fail to disclose an integrated circuit comprising, in combination with other cited limitations, a memory array having a first width, the memory array including a plurality of primary columns and a plurality of redundant columns; a data register coupled to the TDI terminal, the data register having a second width, the second width being less than the first width; and a bitline latch coupled between the data register and the memory array, the bitline latch having the first width as recited in claim 20.

Claims 21-27 are therefore allowed because of their dependency on claim 20.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue

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fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for

Allowance."

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Tuan T. Nguyen whose telephone number is 571-272-1880. The

examiner can normally be reached on Monday - Friday, 8AM - 4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Richard Elms can be reached on 571-272-1869. The fax phone number for the

organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan T Nguyen Primary Examiner Page 4

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January 6, 2006

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